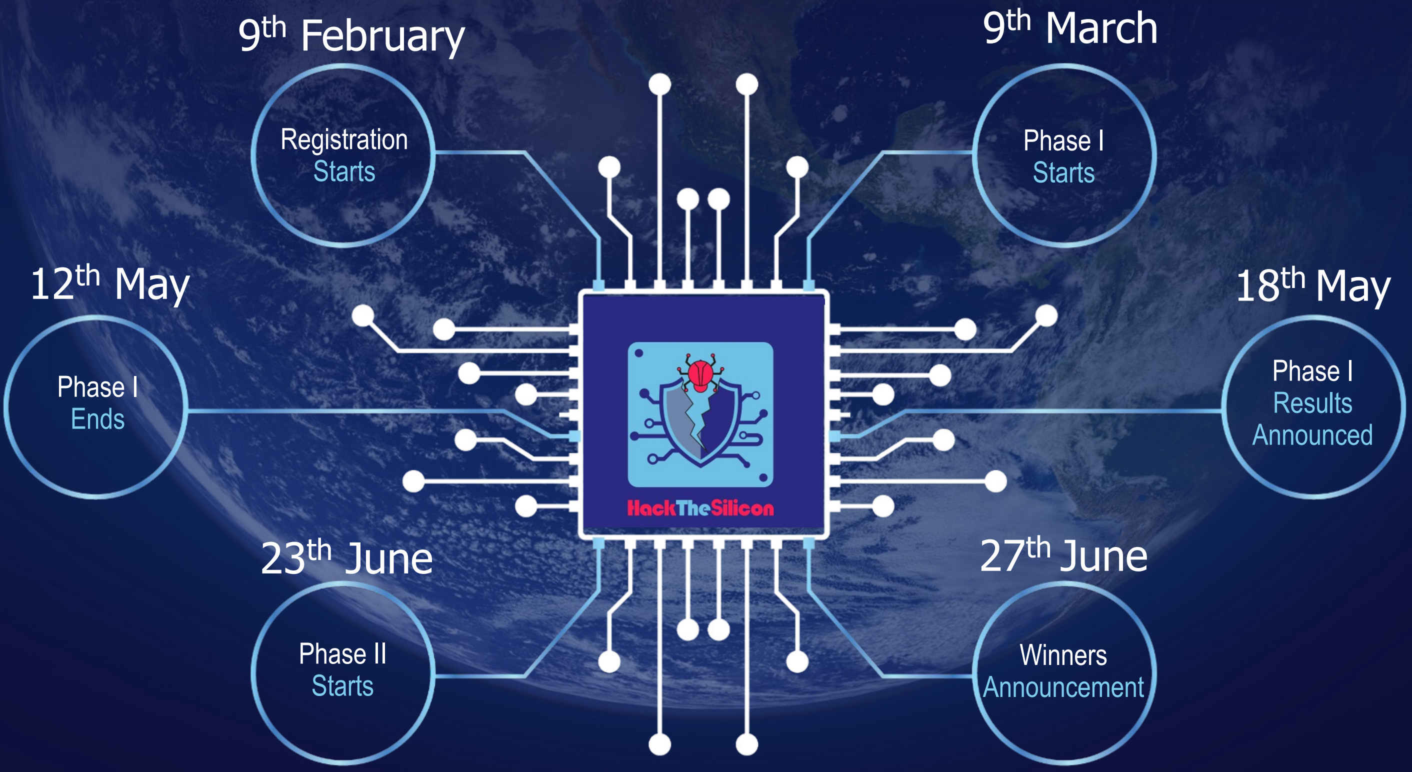
DAC
2024

HACK@DAC'24

The World's Largest Joint Industry-Academia Hardware Security Competition



9th February
Registration Starts

9th March
Phase I Starts

12th May
Phase I Ends

18th May
Phase I Results Announced

23th June
Phase II Starts

27th June
Winners Announcement

Why HACK@DAC?

The growing number of hardware design and implementation vulnerabilities has led to a new attack paradigm that casts a long shadow on decades of research on system security. It disrupts the traditional threat models that focus mainly on software-only vulnerabilities and often assume that the underlying hardware is behaving correctly and is trustworthy.

System-on-Chip (SoC) designers use a mix of third-party and in-house intellectual property (IP) cores. Any security-critical vulnerability in these IPs can undermine the trustworthiness of the whole SoC.

Attacks may cause a system failure or deadlock, remotely access sensitive information, or even gain privileged access to the system, bypassing the in-place security mechanisms.

Participating in HACK@DAC

Participating teams can be from industry, academia, or a combination. They will receive an altered OpenTitan SoC design with planted security vulnerabilities. They must identify these vulnerabilities, assess their impact, provide exploits, and propose mitigation.

The teams can use any tool or technique and should provide a detailed report on their findings. The submitted bug reports will be evaluated based on a scoring system that considers the number and severity of security vulnerabilities, their exploitation, and the used security assurance automation methods and tools.

The competition has two phases. Only the selected teams from the first phase can participate in the final phase during DAC 2024.

For more information about the competition and eligibility requirements, visit our website:

https://hackthesilicon.com/home/hackdac24/

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